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Cadence OrCAD PCB Designer Version 16.3 Professor John H. Davies Department of Electronics and Electrical Engineering Glasgow University, Glasgow, G12 8QQ, Scotland, UK Email: jdavies@elec.gla.ac.uk 2011 October 6 Contents Preamble 2 1 Introduction 3 2 One-transistor amplifier: Schematic capture 5 3 OrCAD PCB Editor 14

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Electronic Design Project 2 Cadence OrCAD PCB Designer Notes for demonstrators Professor John H. Davies* October 28, 2008 Objectives After completing these laboratories, you should be able to: • capture, simulate and layout a simple, one-transistor amplifier on a one-sided printed circuit board (PCB) with manual routing

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Cadence Orcad Pcb Designer University The Cadence OrCAD PCB Designer suite comprises three main applications. • Capture is used to draw the circuit on the screen (schematic capture). A netlist, which describes the components and their interconnections, is the link to PSpice and PCB Editor. • PSpice simulates a captured circuit. I do not ...

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